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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/621,008	07/16/2003	Janardhanan S. Ajit	50717/PAN/B600	9110
23363	7590 12/31/2003		EXAMINER	
CHRISTIE, PARKER & HALE, LLP			LAM, TUAN THIEU	
350 WEST CO SUITE 500	OLORADO BOULEVARD	•	ART UNIT	PAPER NUMBER
PASADENA,	CA 91105		2816	
		•	DATE MAILED: 12/31/200	3

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)				
		10/621,008	AJIT, JANARDH	ANAN S.			
	Office Action Summary	Examiner	Art Unit				
		Tuan T. Lam	2816				
	The MAILING DATE of this communication a	appears on the cover shee	t with the correspondence a	ddress			
Period fo	• •	OLV IO OET TO EVOIDE	2 MANTU(2) EDAM				
THE - Exte after - If the - If NO - Failu - Any	ORTENED STATUTORY PERIOD FOR REF MAILING DATE OF THIS COMMUNICATION nsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a rD period for reply is specified above, the maximum statutory perior to reply within the set or extended period for reply will, by started by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, mareply within the statutory minimum o od will apply and will expire SIX (6) tute, cause the application to become	y a reply be timely filed f thirty (30) days will be considered tim MONTHS from the mailing date of this the ABANDONED (35 U.S.C. § 133).	ely. communication,			
1)⊠	Responsive to communication(s) filed on 16	3 July 2003.					
·		nis action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
4)⊠	Claim(s) 1-4 is/are pending in the application	n.					
	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)	Claim(s) is/are allowed.						
6)⊠	☑ Claim(s) <u>1-4</u> is/are rejected.						
	Claim(s) is/are objected to.						
8)[Claim(s) are subject to restriction and	d/or election requirement					
Applicat	ion Papers						
9)🛛	The specification is objected to by the Exam	iner.					
10)🛛	The drawing(s) filed on $\underline{16 \ July \ 2003}$ is/are:	a)⊠ accepted or b)□ o	pjected to by the Examiner.				
	Applicant may not request that any objection to t	the drawing(s) be held in abo	eyance. See 37 CFR 1.85(a).				
	Replacement drawing sheet(s) including the corr	·					
•	The oath or declaration is objected to by the	Examiner. Note the attac	ched Office Action or form I	PTO-152.			
	under 35 U.S.C. §§ 119 and 120						
a) 13)⊠ 2 3 14)□ 2	Acknowledgment is made of a claim for fore All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the papplication from the International Burn See the attached detailed Office action for a lacknowledgment is made of a claim for domestince a specific reference was included in the 37 CFR 1.78. a) The translation of the foreign language Acknowledgment is made of a claim for domestince was included in the first sentence of the foreign language.	ents have been received. ents have been received priority documents have been (PCT Rule 17.2(a)). list of the certified copies estic priority under 35 U.S. first sentence of the spec- provisional application has estic priority under 35 U.S.	in Application No een received in this National not received. S.C. § 119(e) (to a provision cification or in an Application as been received. S.C. §§ 120 and/or 121 since	nal application) on Data Sheet. se a specific			
Attachme		_					
	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948)		iew Summary (PTO-413) Paper N e of Informal Patent Application (P				
	rmation Disclosure Statement(s) (PTO-1449) Paper No(s			10-102)			

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DETAILED ACTION

Specification

1. The disclosure is objected to because of the following informalities: applicant is required to update the information on the application 10/043,788. The application 10/043,788 is now US patent 6,628,149.

Appropriate correction is required.

Double Patenting

2. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

3. Claims 1-4 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-4 of U.S. Patent No. 6,628,149. Although the conflicting claims are not identical, they are not patentably distinct from each other because they having common technical features.

Regarding claim 1 of the present invention, claim 1 of USP 6,628,149 recites an upper

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pair of PMOS devices (column 10, lines 1-5), a lower pair of NMOS devices (column 10, lines 7-10), a first bias circuit (column 10, lines 13-15), a second bias circuit (column 10, lines 17-20), a third bias circuit (column 10, lines 23-25), a fourth bias circuit (column 10, lines 29-32).

Regarding claim 2 of the present invention, claim 2 of USP 6,628,149 recites a well biasing circuit (column 10, lines 36-38).

Regarding claim 3 of the present invention, claim 3 of USP 6,628,149 recites the well biasing circuit provides Vddo (column 10, lines 39-41).

Regarding claim 4 of the present invention, claim 4 of USP 6,628,149 recites a core circuitry (column 10, lines 45).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 703-305-3791. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P CALLAHAN can be reached on 730-308-4876. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Tuan T. Lam Primary Examiner Art Unit 2816